COMP 633 - Parallel Computing

Lecture 17
Oct 26, 2021

BSP (2)
Parallel Sorting in the BSP model

Topics

1. What work remains this semester:
   - written assignment, programming assignment, final exam

2. Quick review of BSP

3. Sorting in BSP

Parallel sorting: problem definition

- **Given**
  - N values, each of size b bits
  - a total order $\leq$ defined on the values

- **Initial distribution**
  - each processor holds $n = N \,/\, p$ values

- **Result**

$proc_0$	$proc_1$	$proc_2$	...	$proc_{p-1}$
V_1	V_{k_1+1}	V_{k_2+1}		$V_{k_{p-1}+1}$
...	...	...		...
V_{k_1}	V_{k_2}	V_{k_3}		V_{k_p}

  - $V_i \leq V_{i+1}$ for all $1 \leq i < N = k_p$
  - generally $k_i = n \bullet i$, i.e. evenly distributed across processors

Parallel sorting: general remarks

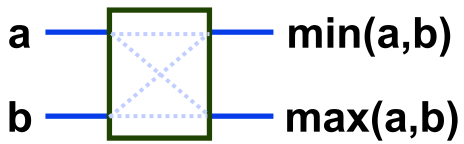
- **Typically concerned with case of *N* >> *p***
  - Small *N* problems don't require parallel processing
  - Use algorithm cascading with efficient sequential sort of *n* elements
    - » sequential radix sort of *n* values has $W^{\text{SORT}}(n) = \Omega(bn)$
    - » sequential comparison-based sort has $W^{\text{SORT}}(n) = \Omega(n \lg n)$ and may be more appropriate when *b* is large
  - Examine scalability in *N* and *p* using BSP model
    - » three parallel algorithms considered
      - Bitonic sort, Radix sort, Sample sort

- **What is the lower bound BSP cost for sorting?**
  - Work bound
    - » (1/p) * optimal sequential work $W^{\text{SORT}}(N)$
  - Communication bound
    - » each value may have to move between processors from input to output
  - BSP lower bound

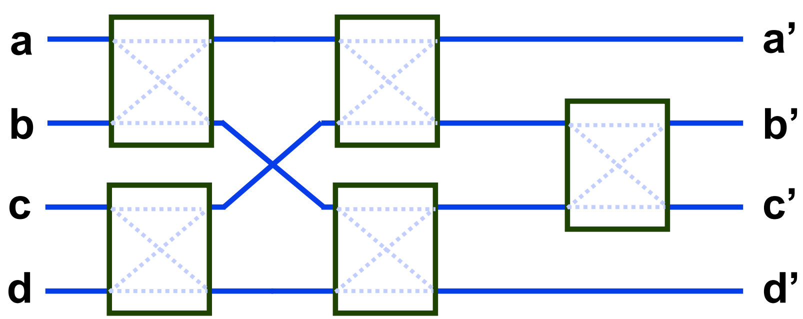
$$C_p^{\text{SORT}}(N,p) \geq \frac{W^{\text{SORT}}(N)}{p} + \frac{N}{p} \cdot g + L$$

Background: Sorting networks for parallel sorting

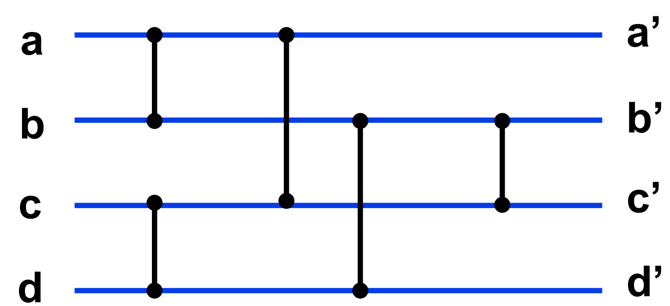
- **Basic component: the *comparator* module**



- **Comparator modules can be connected to form a sorting network**
  - all inputs are presented in parallel
    - » ex: sorting network for 4 values



sorting network

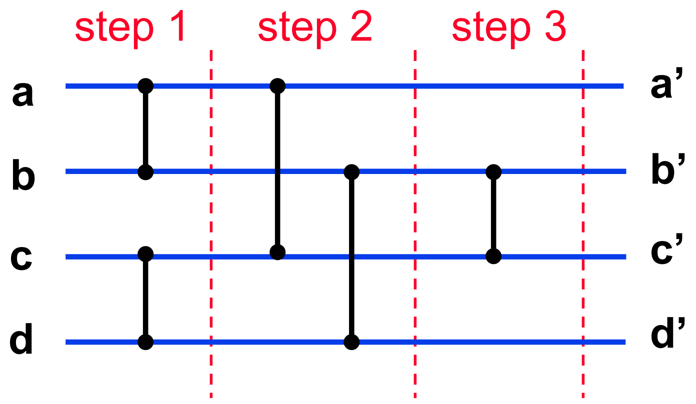


schematic representation

Sorting networks

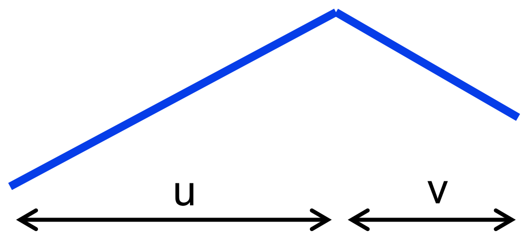
- **Sorting networks are *oblivious***
  - predetermined sequence of comparisons sorts *any* input sequence
  - the depth of a comparator is the maximum number of preceding comparators on any path to an input

- **A sorting network specifies a parallel sorting algorithm**
  - in step i, evaluate all comparators at depth i in parallel
    - » each step permutes inputs to outputs (EREW)
    - » at most n comparators evaluated in each step
      - let d(n) be the depth of a network of size n, then S(n) = d(n), W(n) = O(n·d(n))

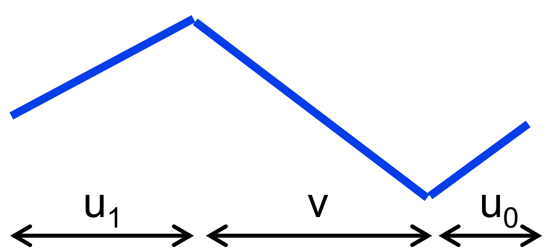


Bitonic Sequence

- **Definitions**
  - A sequence of values *w* is up-down if *w* = *uv* with *u* increasing and v decreasing
    - » ex: w = 1 3 5 9 6 4 3



  - A sequence of values *w* is bitonic if *w* is a circular rotation of an up-down sequence
    - » ex: w = 5 9 6 4 3 1 3



Bitonic sequence theorem

- **Theorem**
  - Suppose *w* is a bitonic sequence of length 2*n* and we define sequences r, s of length *n* as follows

$$r_i = \min(w_i, w_{n+i})$$
$$s_i = \max(w_i, w_{n+i})$$

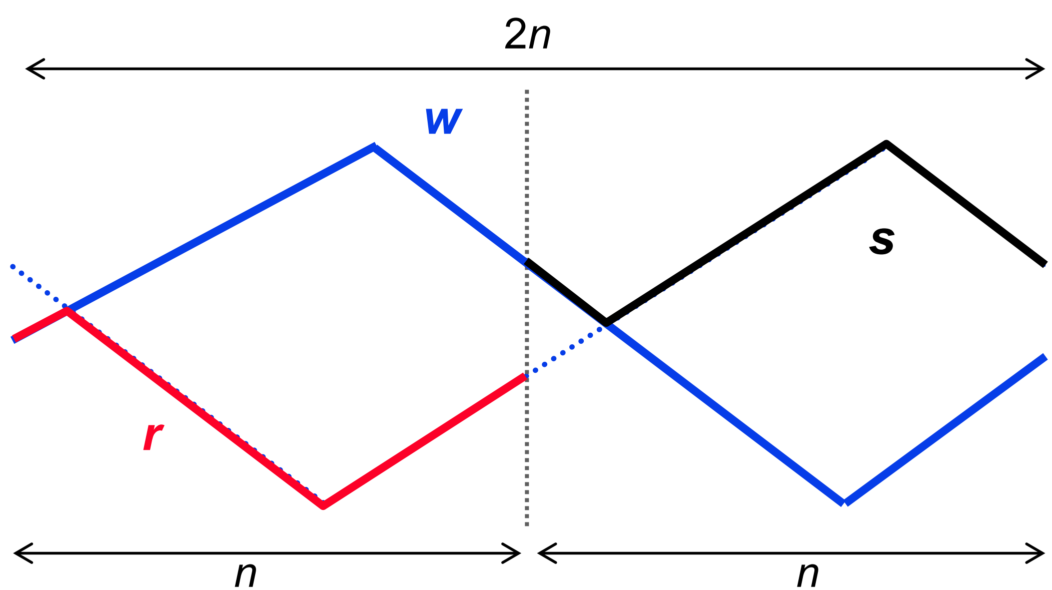
then

(1) $\forall\, 1 \le i, j \le n: \quad r_i \le s_j$ ← partitions the sorting problem !

(2) r, s are both bitonic sequences ← bitonic subproblems !

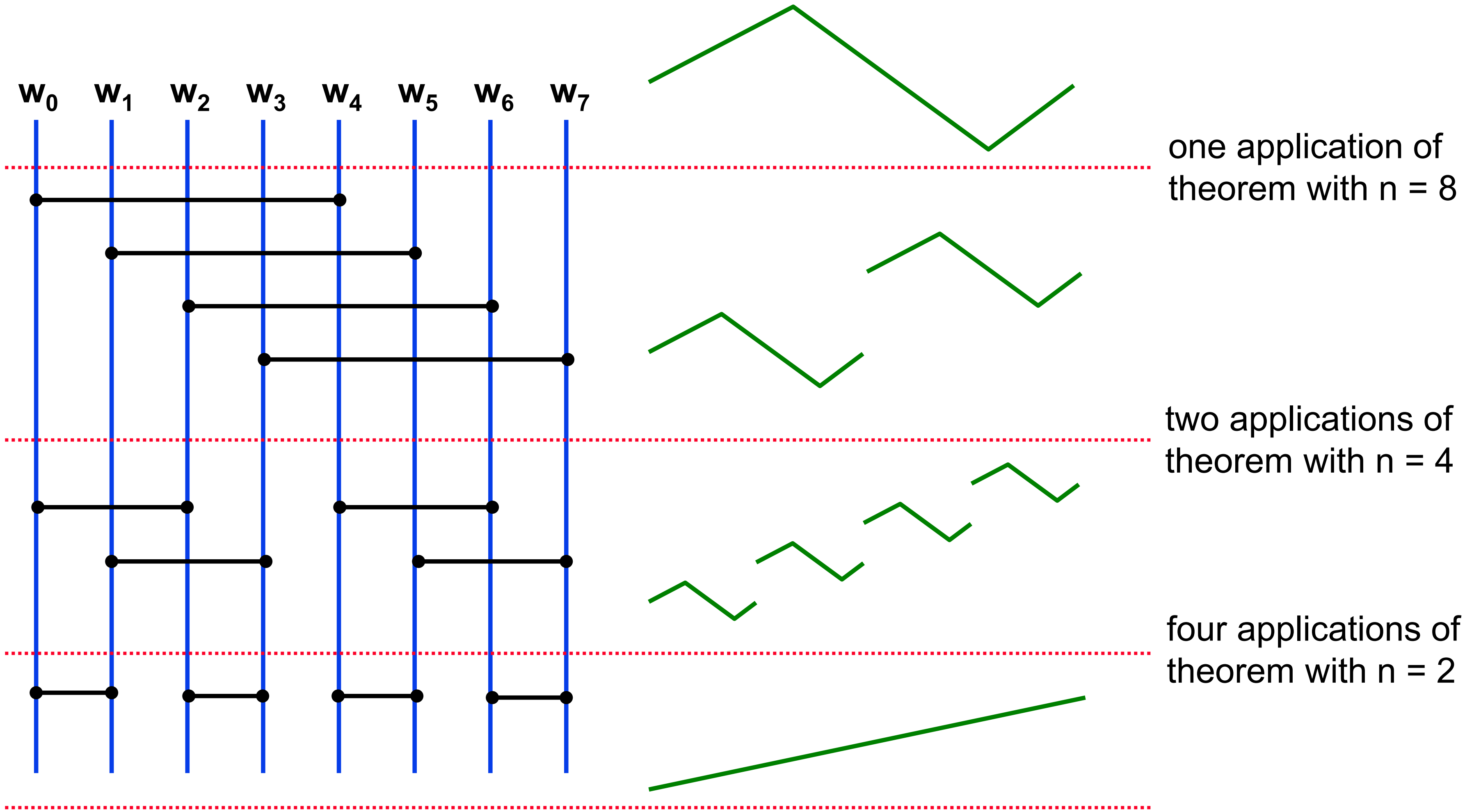
- **Proof**

  (by picture)



Bitonic merge

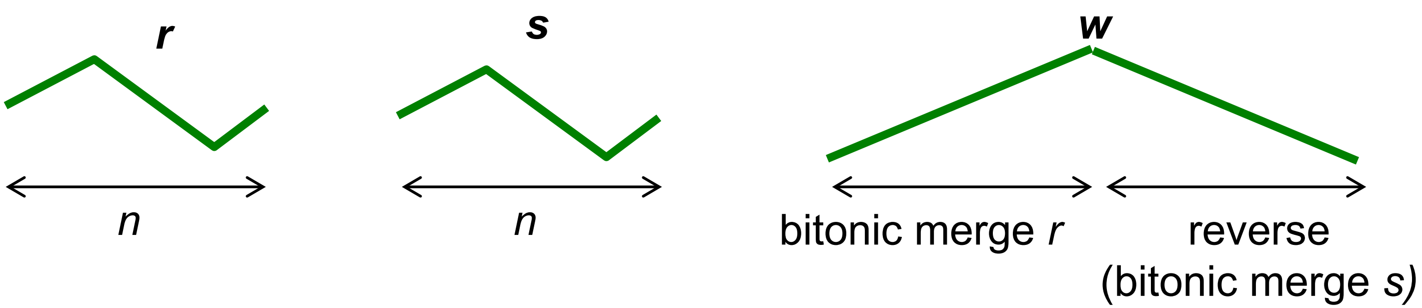
- A bitonic sequence of length $n = 2^k$ can be sorted with a depth k sorting network
  - apply bitonic sequence theorem recursively



Bitonic Sort

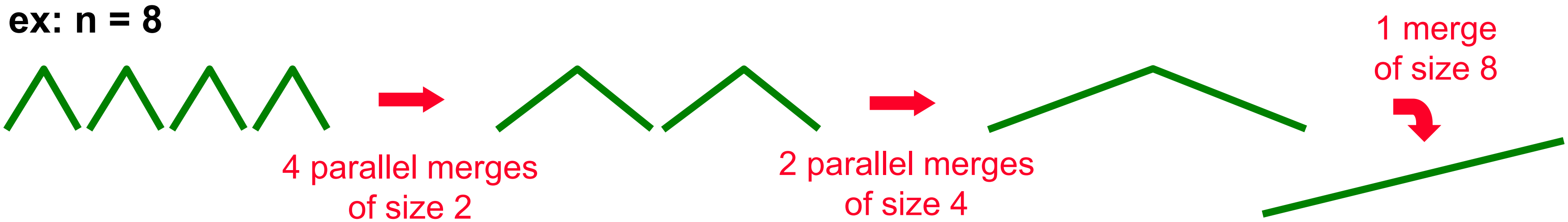
- **Combine two length n bitonic merge sequences to form a length 2n bitonic sequence**
  - given two bitonic sequences *s, r* of length *n* let

    w = (bitonic merge *r*) ++ (reverse (bitonic merge *s*))
  - w is a bitonic sequence of length 2n



- **Bitonic sort of n = 2^k values**
  - view input as n/2 bitonic sequences of length 2
  - combine bitonic sequences k-1 times to create a length n bitonic sequence
  - apply final bitonic merge to yield sorted sequence

- **ex: n = 8**

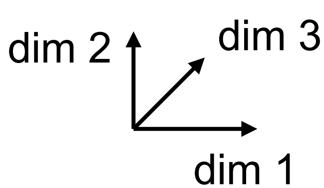


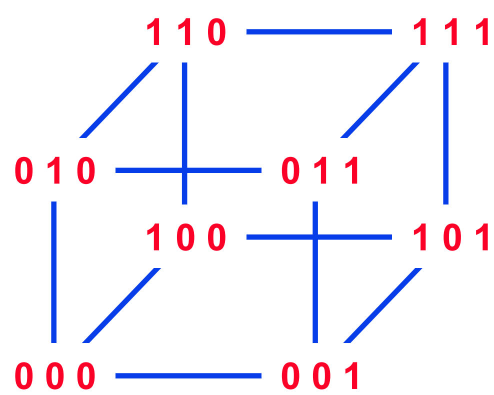
Hypercube communication pattern

- Let $p = 2^k$ for some $k \geq 0$. Processors are numbered $0 \leq h < p$. Let $h^{(j)}$ be the j^{th} bit in the boolean representation of h, where $1 \leq j \leq k$
  - ex p = 8, k = 3 $\quad h^{(3)} \quad h^{(1)}$
    h = 4 = 1 0 0

- For $0 \leq h < p$, processor $nb_j(h)$ is the neighbor of processor h in dimension j. The bits of $nb_j(h)$ are specified as follows, for $1 \leq r \leq k$

$$\{nb_j(h)\}^{(r)} = \begin{cases} h^{(r)} & \textbf{if } r \neq j \\ 1 - h^{(r)} & \textbf{if } r = j \end{cases}$$





Bitonic sort of *A*[0:*p*-1] using *p* processors

- **Assumptions**
  - p = 2^k and *A*[*h*] is stored in variable *a* on processor *h*
  - CE(x,y) = (min(x,y), max(x,y))
- **SPMD program for processor h**

```
for i := 1 to k do
        for j := i downto 1 do
                b := value of a at nb_j(h)
                a,b := CE(a,b)
                if (h^(j) ≠ h^(i+1)) then a,b := b,a
        end do
end do
```

2 supersteps

- **BSP cost** $$C(p) = \sum_{i=1,k} \sum_{j=1,i} \left(O(1) + 1\cdot g + 2\cdot L\right)$$

$$= \left(O(1) + 1\cdot g + 2\cdot L\right) \sum_{i=1,k} \sum_{j=1,i} 1 \quad = \quad \left(O(1) + 1\cdot g + 2\cdot L\right)\frac{k(k+1)}{2}$$

$$= O(\lg^2 p)\left(1 + g + L\right)$$

Extending bitonic sort to $N > p$

- **Simulate larger parallel machine**
  - Let $N = np$ where $n = 2^q$ and $p = 2^k$ so $N = 2^{(k+q)}$

```
for i:= 1 to k+q do
   for j := i downto 1 do
      CE on dimension j
```

- **BSP cost of CE on dimension j**
  - lower dimensions in memory, higher dimensions across processors

$$T_j(n) = \begin{cases} O(n), & \text{if } j \le q \\ O(n) + n \cdot g + L, & \text{if } j > q \end{cases}$$

- **BSP cost for algorithm** $C(N,p) = \sum_{i=1}^{k+q} \sum_{j=1}^{i} T_j(N/p)$

$$= \left(\frac{(\lg N)(1+\lg N)}{2} \right) \cdot O\left(\frac{N}{p} \right) + \sum_{i=q+1}^{k+q} \sum_{j=q+1}^{i} \left(\frac{N}{p} \cdot g + 2L \right)$$

$$= \Theta(\lg^2 N) \cdot \frac{N}{p} + \Theta(\lg^2 p) \cdot \left(\frac{N}{p} \cdot g + 2L \right)$$

Improving work-efficiency

- **What can be done?**
  - first q iterations of outer loop create sorted sequences in processor memories
    - » replace with efficient localsort (O(n) radix sort is assumed here for simplicity)
  - for each value i > q in outer loop, last q iterations of inner loop perform a bitonic merge in processor memories
    - » replace with efficient O(n) sequential algorithm for bitonic merge (`sbmerge`)

- **Updated program**

```
localsort(n)
for i:= q+1 to k+q do
      for j := i downto q+1 do
         CE on dimension j
      sbmerge(n)
```

- **BSP cost**

$$C(N,p) = \Theta\left(\frac{N}{p}\right) + (\lg p)\left(\frac{1+\lg p}{2}\cdot\left(O\left(\frac{N}{p}\right)+\frac{N}{p}\cdot g + 2L\right) + O\left(\frac{N}{p}\right)\right)$$

$$= \Theta(\lg^2 p)\cdot\frac{N}{p} + \Theta(\lg^2 p)\cdot\left(\frac{N}{p}\cdot g + L\right)$$

Improving communication efficiency

- **What can be done?**
  - combine communication for up to lg p successive CE operations

- **Updated program**

```
localsort(n)
for i:= q+1 to k+q do
        transpose(n)
        (i-q) successive CE(n) on local data
        transpose(n)
        sbmerge(n)
```

- **BSP cost**

$$C(N,p) = \Theta\left(\frac{N}{p}\right) + (\lg p)\left(2\left(\frac{N}{p}\cdot g + L\right) + (1+\lg p)\cdot\Theta\left(\frac{N}{p}\right) + \Theta\left(\frac{N}{p}\right)\right)$$

$$= \Theta(\lg^2 p)\cdot\frac{N}{p} + \Theta(\lg p)\cdot\left(\frac{N}{p}\cdot g + L\right)$$

BSP predicted and measured times for bitonic sort

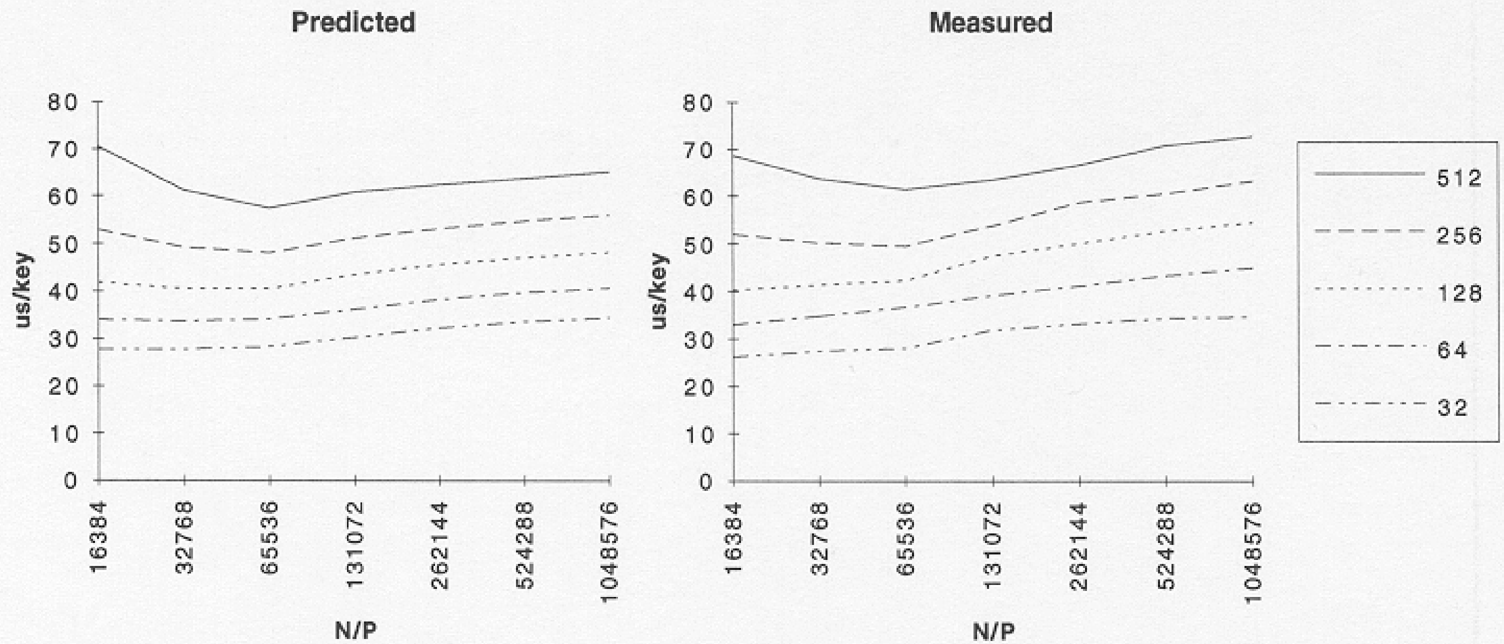


Figure 1.4 *Predicted and measured execution time per key of bitonic sort on the CM-5. Times are shown to sort between 16K and 1M keys per processor on 32, 64, 128, 256 and 512 processors.*

BSP breakdown of time in optimized bitonic sort

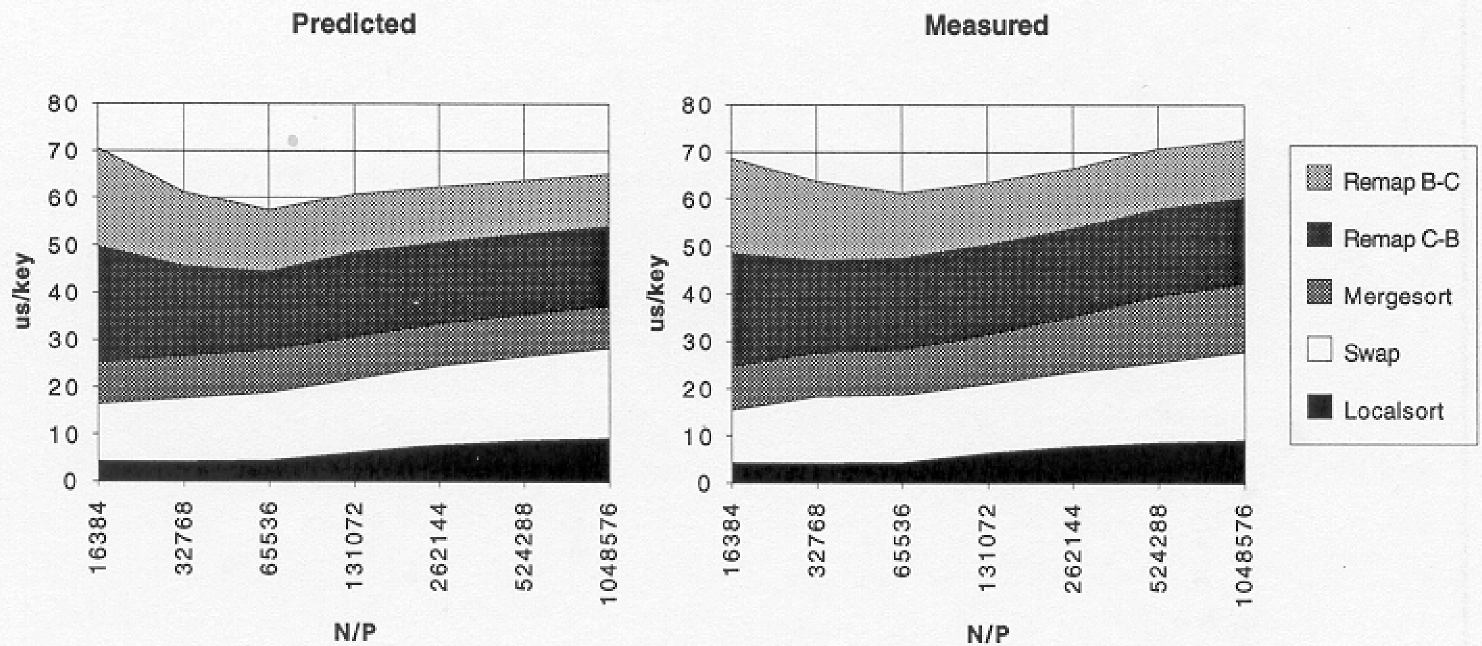


Figure 1.5 *Predicted and measured execution times per key on 512 processors for the phases of bitonic sort. The time for the single gather is included in the time for the remap from a blocked to a cyclic layout; likewise, the time for the scatter is included in the time for the remap from a cyclic to a blocked layout.*